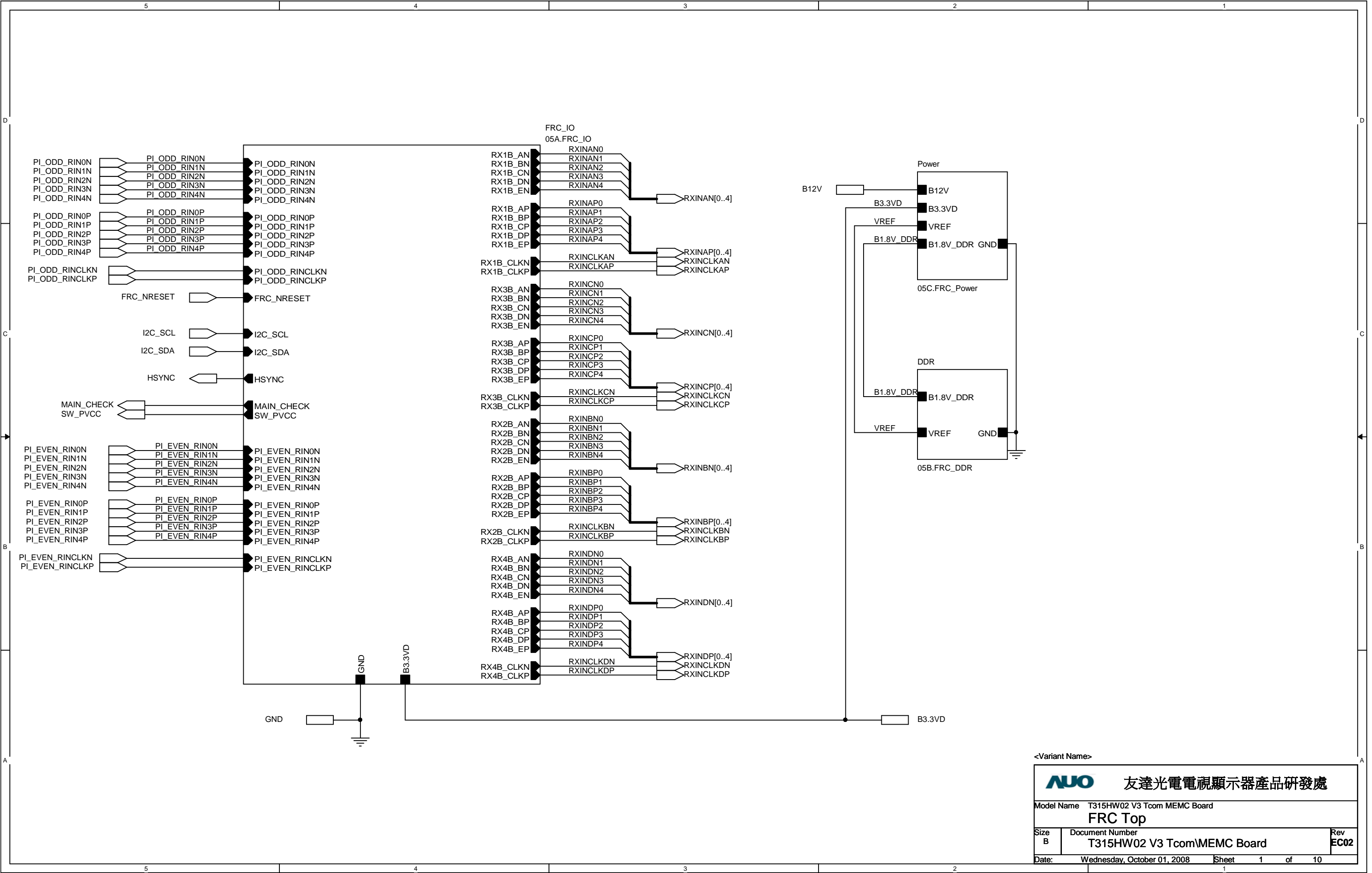
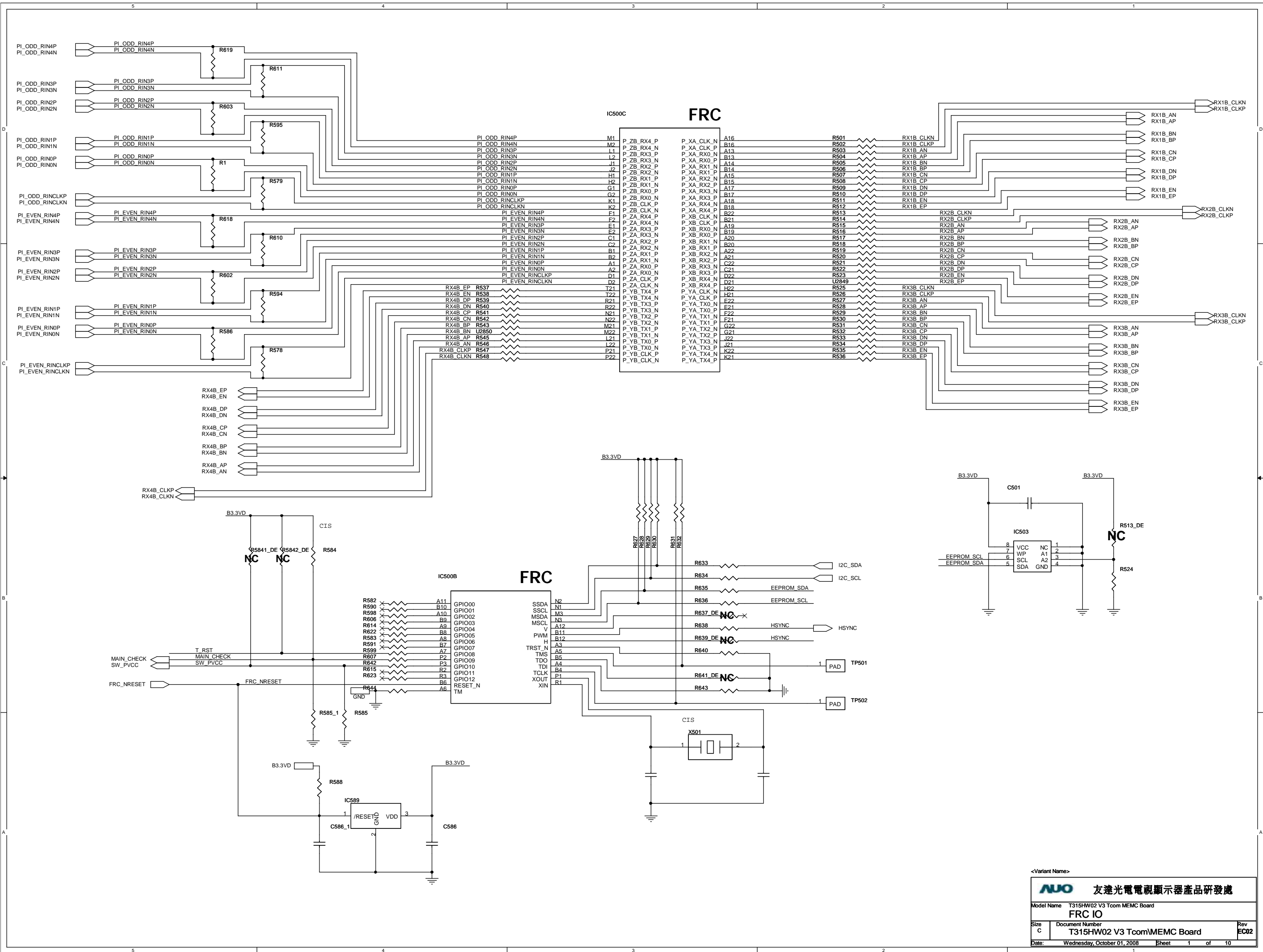
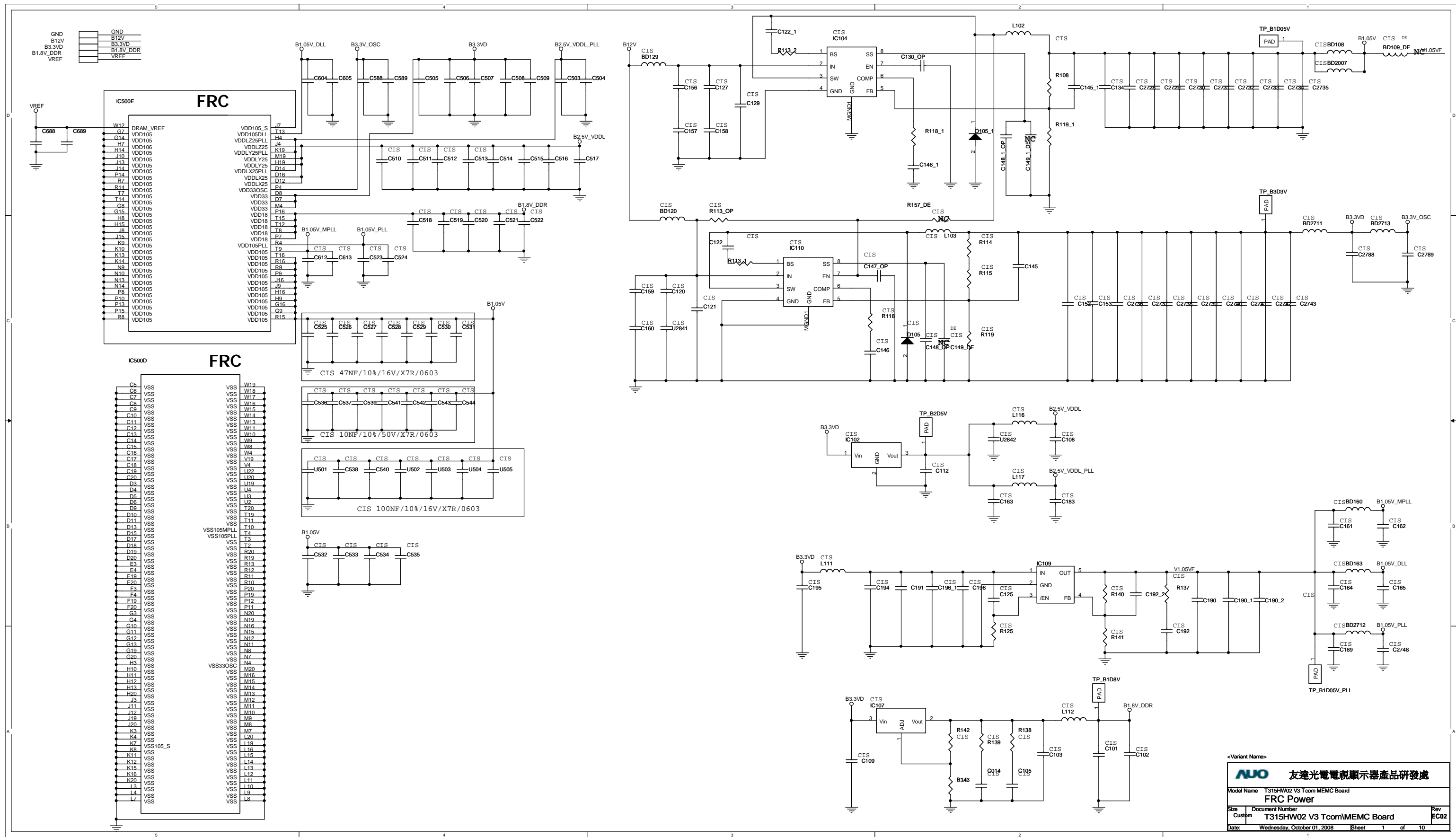


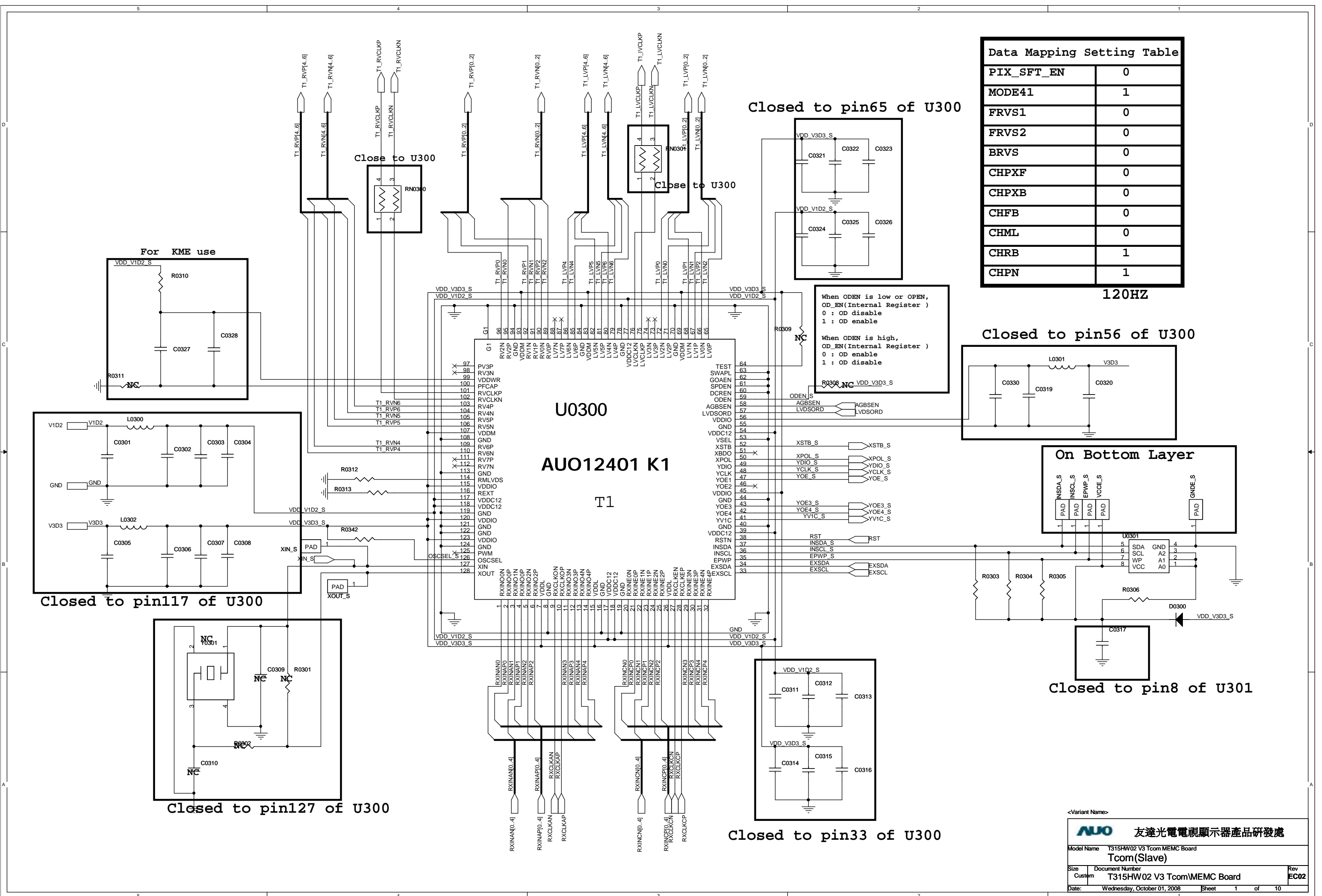
Data Mapping Setting Table	
PIX_SFT_EN	0
MODE41	1
FRVS1	0
FRVS2	0
BRVS	0
CHPXF	0
CHPXB	0
CHFB	0
CHML	0
CHRB	1
CHPN	1

120HZ







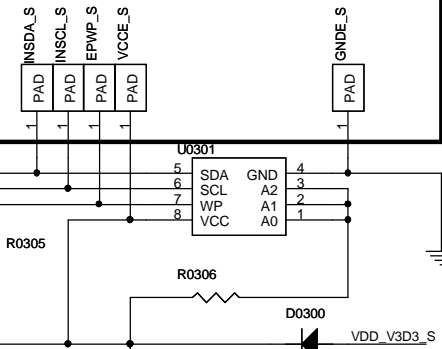


Data Mapping Setting Table	
PIX_SFT_EN	0
MODE41	1
FRVS1	0
FRVS2	0
BRVS	0
CHPXF	0
CHPXB	0
CHFB	0
CHML	0
CHRB	1
CHPN	1

120HZ

Closed to pin56 of U300

On Bottom Layer



Closed to pin8 of U301